# Call for Papers
# HiPEAC 2012

## 7th International Conference on High-Performance and Embedded Architectures and Compilers

HiPEAC COMPILATION ARCHITECTURE

Sponsored by

SEVENTH FRAMEWORK PROGRAMME

**January 23-25, 2012 | Paris, France**
**http://www.hipeac.net/conference**

The challenges faced by the high-performance general-purpose and embedded worlds are converging. The embedded market evolves rapidly, quickly expanding the capabilities of new devices, and the requirements of these new applications demand technologies that not long ago were in the realm of high-performance computing. Conversely, the energy and cost constraints typical of the embedded world are now also among the most important design criteria for general purpose computing systems. Because performance no longer automatically increases with advances in semiconductor technology, it has become essential to discover new paths to optimize performance, energy and cost across software and hardware.

The HiPEAC conference provides a forum for computer and compiler architects in the field of high performance architecture and compilation for embedded and general-purpose systems, with a special emphasis on cross-cutting research that can be applied to both. The conference aims at the dissemination of advanced scientific knowledge and the promotion of international contacts among scientists from academia and industry. Topics of interest include, but are not limited to:

- Processor architectures
- Memory system optimization
- Power, performance and implementation efficient designs
- Reliability and real-time support in processors, compilers and run-time systems
- Network and security processors
- Application-specific processors and accelerators
- Reconfigurable architectures
- Simulation and methodology
- Hardware and run-time support for programming languages
- Compiler techniques
- Feedback-directed optimization
- Program characterization and analysis techniques
- Dynamic compilation, adaptive execution, and continuous profiling/optimization
- Back-end code generation
- Binary translation/optimization
- Code size/memory footprint optimizations

**Submissions of workshops/tutorials, papers and posters:**
**See http://www.hipeac.net/conference and http://taco.acm.org**

**New Publication Model: See Reverse Side**

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### HIGHLIGHTS
- New publication model with ACM TACO (see reverse side)
- Keynote presentations
- Main track of invited presentations
- Parallel tracks with workshops
- Tutorials
- Student poster sessions
- Industrial exhibits
- Updates on ongoing FP7 projects in computing systems

### DEADLINES
- Workshops/tutorials: June 1, 2011
- Papers: July 15, 2011
- Posters: October 15, 2011

## HiPEAC 2012 - Paper track

Each presentation takes 25' plus 5' for questions.

| Session Id | Monday | Session Ttitle |
|---|---|---|
| | **Session Chair: Ben Juurlink** | |
| A | DAPSCO: Distance-Aware Partially Shared Cache Organization<br>Authors: Antonio García-Guirado; Ricardo Fernández-Pascual; Alberto Ros; Jose Manuel García (Spain) | Caches |
| A | Writeback-aware Partitioning and Replacement in Last-Level Cache of Phase Change Main Memory System<br>Authors: Miao Zhou; Yu Du; Bruce Childers; Rami Melhem; Daniel Mosse (United States) | Caches |
| | **Parallel Session I** | |
| | **Session Chair: Lasse Natvig** | |
| M | Sabrewing: a Lightweight Architecture for Combined Floating-Point and Integer Arithmetic<br>Authors: Tom Bruintjes; Karel Walters; Sabih Gerez; Bert Molenkamp; Gerard Smit (Netherlands) | Micro Architecture |
| M | SYRANT: SYmmetric Resource Allocation on Not-taken and Taken Paths<br>Authors: Nathanaël Prémillieu; Andre Seznec (France) | Micro Architecture |
| | Break | |
| | **Session Chair: Avi Mendelson** | |
| B | Hardware Transactional Memory with Software-Defined Conflicts<br>Authors: Ruben Titos-Gil; Manuel E Acacio; Jose Manuel García; Tim Harris; Adrian Cristal; Osman Unsal; Ibrahim Hur; Mateo Valero (Spain) | Transactional Memory |
| B | A Transactional Memory with Automatic Performance Tuning<br>Authors: Qingping Wang; Sameer Kulkarni; John Cavazos; Michael Fuchs Spear (United States) | Transactional Memory |
| B | FlexSig: Implementing Flexible Hardware Signatures<br>Authors: Lois Orosa Nogueira; Elisardo Antelo; Javier D. Bruguera (Spain) | Transactional Memory |
| | Parallel Session II | |
| | **Session Chair: Wolfgang Karl** | |
| N | sFtree: A fully connected and deadlock free switch-to-switch routing algorithm for fat-trees<br>Authors: Bartosz Grzegorz Bogdanski; Sven-Arne Reinemo; Frank Olaf Sem-Jacobsen (Norway) | Parallel Architectures |
| N | Bahurupi: A Polymorphic Heterogeneous Multi-Core Architecture<br>Authors: Mihai Pricopi; Tulika Mitra (Singapore) | Parallel Architectures |
| | Lunch | |
| | **Session Chair: Andre Seznec** | |
| C | ABS: A low-cost adaptive controller for prefetching in a banked shared LLC<br>Authors: Jorge Albericio; Rubén Gran; Pablo Ibáñez; Victor Viñals; Jose M. Llabería (Spain) | Cache and Memory Techniques |
| C | The Migration Prefetcher: Anticipating Data Promotion in Dynamic NUCA Caches<br>Authors: Javier Lira; Timothy M. Jones; Carlos Molina; Antonio Gonzalez (United States) | Cache and Memory Techniques |
| C | The Gradient-Based Partitioning Algorithm<br>Authors: William Hasenplaugh; Joel Emer; Aamer Jaleel; Pritpal Ahuja; Simon Steely (United States) | Cache and Memory Techniques |
| | Break | |
| | **Session Chair: Manolis Katevenis** | |
| D | Efficiently Exploiting Memory Level Parallelism on Asymmetric Multicore Processors<br>Authors: George Patsilaras; Niket Choudhary; James Tuck (United States) | Multi-Core and GPGPUs |
| D | Optimizing Explicit Data Transfers for Data Parallel Applications on the Cell Architecture<br>Authors: Selma Saidi; Pranav Tendulkar; Thierry Lepley; Oded Maler (France) | Multi-Core and GPGPUs |
| D | Exploring the Limits of GPGPU Scheduling In Control Flow Bound Applications<br>Authors: Roman Malits; Avinoam Kolodny; Avi Mendelson; Evgeny Bolotin (Israel) | Multi-Core and GPGPUs |

| Session Id | Tuesday | Session Ttitle |
|---|---|---|
| | **Session Chair: Koen Bertels** | |
| E | Using Machine Learning to Improve Automatic Vectorization | Loop Transformations |

| | | |
|---|---|---|
| | Authors: Kevin Stock; Louis-Noel Pouchet; P. Sadayappan (United States) | |
| E | Polyhedral Parallelization of Binary Code<br>Authors: Benoit Pradelle; Alain Ketterlin; Philippe Clauss (France) | Loop Transformations |
| | Break | |
| **Session Chair: Lieven Eeckhout** | | |
| F | Compiler Techniques to Improve Dynamic Branch Prediction for Indirect Jump and Call Instructions<br>Authors: Jason McCandless (Ireland) | Back-End Compilation |
| F | Efficient Liveness Computation Using Merge Sets and DJ Graphs<br>Authors: Dibyendu Das; Benoit Dupont de Dinechin; Ramakrishna Upadrasta (India) | Back-End Compilation |
| F | Approximate Clustering of Program Graphs and its Application to Compilers<br>Authors: John Demme; Simha Sethumadhavan (United States) | Back-End Compilation |
| | Lunch | |
| **Session Chair: Alex Ramirez** | | |
| G | Toward High Throughput Algorithms on Many Core Architectures<br>Authors: Daniel Alberto Orozco; Elkin Eduardo Garcia; Rishi Khan; Kelly Livingston; Guang Gao (United States) | Runtime Systems |
| G | Seamlessly Portable Applications: Managing the Diversity of Modern Heterogeneous Systems<br>Authors: Mario Kicherer; Fabian Nowak; Rainer Buchty; Wolfgang Karl (Germany) | Runtime Systems |
| G | Thread Tranquilizer: Dynamically Reducing Performance Variation<br>Authors: Kishore Kumar Pusukuri; Rajiv Gupta; Laxmi Bhuyan (United States) | Runtime Systems |
| | Break | |
| **Session Chair: Yiannakis Sazeides** | | |
| H | An Architecture-Independent Instruction Shuffler to Protect against Side-Channel Attacks<br>Authors: Ali Galip Bayrak; Nikola Velickovic; Paolo Ienne; Wayne Burleson (Switzerland) | Security |
| H | Non-Monopolizable Caches: Low-Complexity Mitigation of Cache Side Channel Attacks<br>Authors: Leonid Domnister; Aamer Jaleel; Jason Loew; Dmtry Ponomarev; Nael Abu-Ghazaleh (United States) | Security |
| H | Compiler Mitigations for Time Attacks on Modern x86 Processors<br>Authors: Jeroen Van Cleemput; Bart Coppens; Bjorn De Sutter (Belgium) | Security |

| Session Id | Wednesday | Session Ttitle |
|---|---|---|
| **Session Chair: Cristina Silvano** | | |
| I | VSim: Simulating Multi-Server Setups at Near Native Hardware Speed<br>Authors: Frederick Ryckbosch; Stijn Polfliet; Lieven Eeckhout (Belgium) | Architecture Simulation |
| I | On the Simulation of Large-scale Architectures Using Multiple Application Abstraction Levels<br>Authors: Alejandro Rico; Felipe Cabarcas; Carlos Villavieja; Milan Pavlovic; Augusto Vega; Yoav Etsion; Alex Ramirez; Mateo Valero (Spain) | Architecture Simulation |
| | Break | |
| **Session Chair: Stefanos Kaxiras** | | |
| J | PLDS: Partitioning Linked Data Structures for Parallelism<br>Authors: Min Feng; Changhui Lin; Rajiv Gupta (United States) | Data and I/O Management |
| J | Dynamic Structure Layout Optimizations for Heap Objects<br>Authors: Zhenjiang Wang; Chenggang Wu; Pen-Chung Yew (China) | Data and I/O Management |
| J | ReNIC: Architecture Extension to SR-IOV Network for Efficient High Availability Replication<br>Authors: Yaozu Dong; Yu Chen; Zhenhao Pan; Jinquan Dai; Yunhong Jiang (China) | Data and I/O Management |
| | Lunch | |
| **Session Chair: Sascha Uhrig** | | |
| K | Multithreaded COTS Processors for Time-Critical Environments<br>Authors: Petar Radojkovic; Sylvain Girbal; Arnaud Grasset; Eduardo Quiñones; Francisco Cazorla; Sami Yehia (Spain) | Real-Time and Energy |
| K | TL-Fluid-Based Multi-Core Energy-Efficient Scheduling for Sporadic Real-Time Tasks<br>Authors: Dong Song Zhang; Deke Guo; Fangyuan Chen; Tong Wu; ShiYao Jin (China) | Real-Time and Energy |
| K | Utilizing RF-I and Intelligent Scheduling for Better Throughput/Watt in a Mobile GPU Memory System<br>Authors: Kanit Therdsteerasukdi; Gyungsu Byun; Jason Cong; Frank Chang; Glenn Reinman (United States) | Real-Time and Energy |
| | Break | |
| **Session Chair: Bilha Mendelson** | | |
| L | Toward an Accelerator-Based Architecture<br>Authors: Michael John Lyons; Mark Hempstead; Gu-Yeon Wei; David Brooks (United States) | Accelerator Architectures |
| L | Improving Performance of Nested Loops on Reconfigurable Array Processors<br>Authors: Yongjoo Kim; Jongeun Lee; Toan Mai Xuan; Yunheung Paek (Korea, Republic of) | Accelerator Architectures |
| L | Making wide-issue VLIW Processors Viable on FPGAs<br>Authors: Madhura Purnaprajna; Paolo Ienne (Switzerland) | Accelerator Architectures |

N

# Ninth European Dependable Computing Conference - EDCC 2012

**Sibiu, Romania, May 8-11, 2012**



**General Chair**
**Cristian Constantinescu**
AMD, U.S. / Romania

**Program Chair**
**Miguel Correia**
IST / INESC-ID, Portugal



## CALL FOR PAPERS

printable CFP

**Important dates**

Paper submission: ~~October 17, 2011 October 17, 2011~~
Author notification: January 17, 2012
Final version of papers: February 15, 2012

**Overview**

The 21st century society relies on computing systems more than ever. Computers are no longer simply machines that are used by organizations or at home. They are embedded everywhere, from cell phones to cars or industrial control devices, and large-scale cloud computing providers are sharing them among many organizations in an unprecedented scale. As computers have become indispensable, their failures may significantly perturb our daily lives. The increased hardware and software complexity, as well as the scaling of manufacturing technologies towards nanometer size devices, pose new challenges to the developers. As a consequence the development, testing, and benchmarking of dependable systems has become a vital topic of research, both for academia and industry.

EDCC is the leading European conference for presenting and discussing the latest research in dependable computing. As in previous years, its ninth edition aims at providing a European-hosted venue for researchers and practitioners from all over the world to present and discuss their latest research results on dependability, security, fault-tolerance, and testing. Original papers are solicited on theory, techniques, systems, and tools for the design, validation, operation and evaluation of dependable and secure computing systems, covering any fault model, from traditional hardware and software faults to accidental and malicious human interactions.

EDCC-2012 will have as main theme:

**"From small to large: from Embedded System to Cloud Computing Dependability"**

Nevertheless, the conference will cover a much broader range of topics, which includes, but is not limited to:

* Hardware and software architecture of dependable systems;
* Safety critical systems;
* Embedded and real-time systems;
* Cloud computing reliability and security;
* Dependable mobile and multimedia systems;
* Fault-tolerant networks and protocols;
* VLSI dependability;
* Impact of manufacturing technology on dependability;
* Dependability modeling and tools;
* Testing and validation methods;

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CPS Conference Publishing Services

# Call for Papers

We invite you to consider submitting papers to the 20th Euromicro International Conference on Parallel, Distributed, and Network-Based Processing PDP 2012 that will be held at the Leibniz Supercomputing Centre, Garching near Munich, Germany from 15th to 17th of February, 2012.

Parallel, Distributed, and Network-Based Processing has undergone impressive change over recent years. New architectures and applications have rapidly become the central focus of the discipline. These changes are often a result of cross-fertilisation of parallel and distributed technologies with other rapidly evolving technologies. It is of paramount importance to review and assess these new developments in comparison with recent research achievements in the well-established areas of parallel and distributed computing, from industry and the scientific community.
The PDP 2012 aims to provide a forum for the presentation of these and other issues through original research presentations and to facilitate the exchange of knowledge and new ideas at the highest technical level. Topics of interest include, but are not restricted to:

- **Parallel Computing**: massively parallel machines; embedded parallel and distributed systems; multi- and many-core systems; GPU and FPGA based parallel systems; parallel I/O; memory organization
- **Distributed and Network-based Computing**: Cluster, Grid, Web and Cloud computing; mobile computing; interconnection networks
- **Data Intensive Computing**: large scale data processing; distributed databases and archives; large scale data management; metadata; data intensive applications
- **Models and Tools**: programming languages and environments; runtime support systems; performance prediction and analysis; simulation of parallel and distributed systems
- **Systems and Architectures**: novel system architectures; high data throughput architectures; service-oriented architectures; heterogeneous systems; shared-memory and message-passing systems; middleware and distributed operating systems; dependability and survivability; resource management
- **Advanced Algorithms and Applications**: distributed algorithms; multi-disciplinary applications; computations over irregular domains; numerical applications with multi-level parallelism; real-time distributed applications; distributed business applications

## Special Sessions

The following special sessions will be organized within the conference:

- **Cloud Computing for Compute and Data Intensive Applications** (chairs Thierry Delaitre and Sebastien Goasguen)
- **On-chip Parallel and Network-based Systems** (chairs Hamid Sarbazi-Azad and Nader Bagherzadeh)
- **Energy-aware Systems** (chairs Bernd Freisleben and Matthias Schmidt)
- **GPU Computing and Hybrid Computing** (chairs Didier El Baz and Michael Hübner)
- **Security in Networked and Distributed Systems** (chair Igor Kotenko)
- **Next Generation of Web Computing** (chair Konrad Klöckner)
- **Modeling, Simulation, and Optimization of Peer-to-peer Environments** (chairs Julien Bourgeois and Etienne Riviere)
- **Grid, Parallel and Distributed Bioinformatics Applications** (chairs Daniele D'Agostino and Ivan Merelli)
- **Cross-Fertilisation of Parallel and Metaheuristics Techniques** (chair Coromoto León)
- **Parallel and distributed storage systems** (chairs Kathrin Peter and Peter Sobe)

## Paper Submission

Papers can be submitted to the conference main track or to Special Sessions through the EasyChair conference submission system **http://www.easychair.org/conferences/?conf=pdp2012** with an indication of the main track or the name of the Special Session. Papers will undergo a blind peer review process.
Prospective authors should submit a full paper not exceeding 5000 words in length and including a 150-200 word abstract. To facilitate an anonymous reviewing process, the first page of the paper should contain only the title and abstract. Moreover, the names of the authors of the paper must not appear in citations in the reference section.
All accepted papers will be included in the same volume, published by **Conference Publishing Services (CPS)**. The Final Paper Preparation and Submission Instructions will be published after the notification of acceptance.
Authors of accepted papers are expected to register and to present the paper at the Workshop.

## Important Dates

Papers due: ~~July 25, 2011~~ August 15, 2011 ( closed)
Acceptance notification: ~~October 10, 2011~~ October 17, 2011
Camera-ready versions: November 11, 2011

## Program Chairs

- **Rainer Stotzka**
- **Yiannis Cotronis**
- **Michael Schiffers**



**6 Mar 2012:**
The **Photo Gallery** is now online.

**28 Feb 2012:**
The **program** contains now links to most of the presentations.

GMM ITG

24. Workshop

# Testmethoden und Zuverlässigkeit von Schaltungen und Systemen

26. bis 28. Februar 2012, Lindner Congress Hotel, Cottbus

## Call for Papers

### Programmkomitee

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Der Workshop „Testmethoden und Zuverlässigkeit von Schaltungen und Systemen“ ist das bedeutendste deutschsprachige Forum, um Trends, Ergebnisse und aktuelle Probleme auf dem Gebiet des Tests, der Diagnose und der Zuverlässigkeit digitaler, analoger, Mixed-Signal- und HF-Schaltungen zu diskutieren. Der Austausch von Ideen ist ein wichtiges Anliegen des Workshops. Erwünscht sind Beiträge aus der industriellen Praxis und von Forschungseinrichtungen. Sowohl sehr stark praxisbezogene Erfahrungsberichte und Ergebnisse als auch theoretische Beiträge sind willkommen.

**Beiträge zu folgenden und weiteren Themen werden erbeten:**

- Defekt- und Fehlermodellierung
- Testerzeugung und Fehlersimulation
- Testgerechter Entwurf
- Selbsttest für Module und Systeme
- Test von HF-Schaltungen
- Signalintegrität
- Systemtest und -zuverlässigkeit
- Diagnose von Ausfallursachen
- Selbstreparatur und Selbstheilung
- Fehlertoleranz und Online-Test
- Robuste und strahlenresistente Systeme
- Test mechatronischer Systeme
- Automatisches Test-Equipment und Testmodellierung
- Testkosten

Der Workshop findet im Lindner Congress Hotel statt und wird vom Lehrstuhl Technische Informatik der Brandenburgischen TU Cottbus in Zusammenarbeit mit dem Leibniz Institut für Innovative Mikroelektronik (IHP) aus Frankfurt / Oder durchgeführt. Interessenten werden gebeten, ihren Beitrag im Umfang von maximal 6 Seiten über die Workshop-Homepage einzureichen. Der Beitrag sollte den Zweck der Arbeit, den Neuigkeitsgehalt und Aspekte der Anwendung beschreiben. Angenommene Beiträge werden auf Wunsch in die Workshop Handouts aufgenommen.

Um einen freien Austausch von Ideen und Informationen zu erleichtern, sind während des Workshops Video- und Audioaufzeichnungen nicht gestattet. Die Sprache des Workshops ist deutsch, Beiträge oder Vorträge in englischer Sprache sind jedoch willkommen.

**Workshop-Homepage**
http://tuz12.informatik.tu-cottbus.de

**Einreichung der Beiträge**
bis spätestens ~~**30. Oktober 2011**~~ **08. November 2011** über die Workshop-Homepage

**Veranstalter**
GI FA 3.5 / ITG FA 8.2 / GMM FA 6.5 RSS Kooperationsgemeinschaft Rechnergestützter Schaltungs- und Systementwurf, BTU Cottbus, Leibniz-Institut für Innovative Mikroelektronik (IHP)

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**GI/ITG/GMA Technical Committee "Dependability and Fault Tolerance" (VERFE)**
and
**DFG Priority Program SPP 1500 "Dependable Embedded Systems"**

# 8th Workshop on Dependability and Fault Tolerance (VERFE'12)

Focus topics "Dependable Embedded Systems" and
"Software-Controlled Adaptive Fault Tolerance in Microprocessors"

in conjunction with
ARCS 2012, Munich, February 28 - March 02, 2012

# Call for Papers

## Background and Focus

Although the basic reliability of hardware and software components over the decades has steadily grown, their increasing number still causes severe problems. Moreover, in recent years it can be observed that in an increasing number of devices, e.g. cars, digital components are integrated into environments of other physical components. Here the complexity of the interactions with these other components, as well as the limited accessibility of the digital ones create problems with regard to maintaining a dependable operation of the entire system in case of faults or external disturbances.

While this is not a problem with microprocessors, there, the ever shrinking feature sizes, the higher complexity, lower voltages, and higher clock frequencies increase the probability of design-, manufacturing-, and operational faults, making fault tolerance techniques in general purpose processors to be of crucial importance in the future. As simple solutions (such as TMR) easily can get too expensive, the ability to trade increased reliability against performance/power overhead will become important, resulting in light-weight fault tolerance techniques implemented in hardware, but controllable from higher software layers.

This workshop aims at presenting contributions and work-in-progress from the research area of dependable and fault tolerant computing in order to bring together scientists working in related fields, especially from the central European countries.

## Topics

Contributions on the topics of "Dependable Embedded Systems" and "Software-Controlled, Adaptive Fault Tolerance in Microprocessors" are of particular interest; contributions on general topics of dependability and fault tolerance are also welcome but not limited to

- reliability models for hardware and software
- modeling and simulation of fault-tolerant systems
- fault-tolerant systems and system components
- formal verification of systems
- testing of hardware and software
- fault treatment
- detection and correction of transient faults
- quantitative assessment of reliability improvements
- safety-critical applications
- timeliness problems
- dependability of networks
- dependability of embedded systems
- highly available systems
- dependable organic computing
- self-organization within redundant systems
- dependable ubiquitous and pervasive computing
- composability of dependable systems
- dependable mechatronic systems / micro systems
- dependability of mobile and wireless systems
- robustness and robustness metrics
- validation and verification
- fault models and fault model abstraction
- fault-injection techniques
- software-controlled fault tolerance
- on-chip backward recovery techniques (e.g. pipeline flush and re-execution)
- forward recovery techniques (notification of higher layers)
- fault-tolerant caches
- dynamic re-use of currently unused resources in processors for fault-tolerance

## Information for Authors

The workshop will focus on research presentations as well as brainstorming sessions. Therefore, two kinds of contributions are welcome:

- research papers documenting results of scientific investigations
- position papers proposing strategies or discussing open problems

An extended abstract (3-4 pages) should be sent as PDF file no later than **November 21, 2011** to:

http://itec.kit.edu/ARCS_dependability_WS/submit/

Notification will be sent by December 17, 2011. The camera-ready version (max. 10 pages) has to be sent no later than **January 20, 2012**; it will appear as part of a separate ARCS 2012 Workshop Proceedings. Conference languages are English and German.

VERFE'12 Workshop site: http://itec.kit.edu/ARCS_dependability_WS
Further information about ARCS 2012: http://www.arcs2012.tum.de/

## VERFE'12 Workshop Chairs

K.-E. Großpietsch, St. Augustin, DE
J. Henkel, Karlsruhe, DE

## VERFE'12 Program Committee

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EUROSYS 2012

APRIL 10-13, 2012
BERN, SWITZERLAND

## WELCOME

EuroSys 2012 is organized by EuroSys, the European Chapter of SIGOPS, sponsored by ACM SIGOPS.

The EuroSys conference series brings together professionals from academia and industry. It has a strong focus on systems research and development: operating systems, data base systems, real-time systems and middleware for networked, distributed, parallel, or embedded computing systems. EuroSys has become a premier forum for discussing various issues of systems software research and development, including implications related to hardware and applications.

EuroSys 2012 will follow the pattern established by the previous EuroSys conferences, by seeking papers on all aspects of computer systems. EuroSys 2012 will also include a number of workshops to allow junior and senior members of the systems community to explore leading-edge topics and ideas before they are presented at a conference.

You can download the flyer (pdf) with the preliminary call for papers and the call for participation (plain text).

## IMPORTANT DATES

All deadlines are at 23:59 CEST (Central European Summer Time).

| | |
|---|---|
| Paper pre-submission: | ~~September 20, 2011~~ |
| Abstract submission: | ~~October 14, 2011~~ |
| Full paper submission: | ~~October 20, 2011~~ |
| Reviews released to authors: | ~~December 24, 2011~~ |
| Rebuttals due: | ~~January 1, 2012~~ |
| Notification to authors: | ~~January 8, 2012~~ |
| Early registration: | March 10, 2012 |
| Tutorials and workshops: | April 10, 2012 |
| Conference: | April 11-13, 2012 |

## BERN

EuroSys 2012 will take place in the charming capital of Switzerland, Bern. The city lies on a peninsula formed by the fast-flowing river Aare. The historic old town, with its preserved medieval street plan, arcades, fountains and towers, is listed as a UNESCO World Heritage Site. Bern is the seat of Swiss federal government. It is ranked among the cities that offer the best quality of life in the world. The Swiss capital has an outstandingly broad range of cultural activities, amongst which a large body of excellent museums, including the renowned Zentrum Paul Klee, the Albert Einstein exhibitions, and the Museum of Communication.

The Bernese Alps, with their world-famous summits Eiger, Mönch, Jungfrau, are easily reachable from the city by train. They host the highest railway station in Europe (3,454 meters).

## CONFERENCE LOCATION

The conference will be organized in the facilities of the University of Bern. With about 15,000 students, the Swiss capital's university enjoys special recognition in leading-edge disciplines and is reputed for the excellent quality of its teaching. It offers a delightful setting and a campus environment intimately linked to the social, economic and political life of the city. With its central geographic location and attractive programs, the University of Bern draws students from all Switzerland's language communities and from abroad.

### NEWS

**[16/02/12]** List of accepted papers online.

**[14/02/12]** Call for grants and posters/demos open.

**[31/01/12]** Keynote presentation announced.

**[25/01/12]** Registration open. Accommodation, travel and visa information.

**[23/01/12]** List of tutorials online.

**[16/01/12]** The PC accepted 27 out of 178 submissions (15.16%), program to be announced after completion of the shepherding process.

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### ORGANIZATION

### SUPPORTERS

# 17th IEEE Workshop on Dependable Parallel, Distributed and Network-Centric Systems

**Shanghai, China**

**Held in conjunction with the**

**26th IEEE International Parallel and Distributed Processing Symposium**

**May 21-25, 2012**

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## Tentative Program (PDF file)

**8:45am - 9:00am Opening Remarks**

**9:00am - 10:00am Dependable Wireless and Sensor Networks (1)**

9:00am "Using Localization for Fault-tolerant Radio Coverage in Wireless Mesh Networks", Svilen Ivanov (rt-solutions.de GmbH, Germany), Edgar Nett (University Magdeburg, Germany)

9:30am "Joint and Simultaneous K-sensing Detection in Deterministic and Random Sensor Networks", Yun Wang (Bradley University, USA), Andrew Kutta (Southern Illinois University, Edwardsville, USA)

**10:00am - 10:30am Coffee Break**

**10:30am - 11:00am Dependable Wireless and Sensor Networks (2)**

10:30am "A Fault-aware Sensor Architecture for Cooperative Mobile Applications", Joerg Kaiser, Sebastian Zug (University Magdeburg, Germany)

11:00am "Network Coding-Based On-Demand Multipath Routing in MANET", Baolin Sun, Xiaocheng Lu, Chao Gui, Ying Song (Hubei University of Economics, China), Hua Chen (Wuhan Textile University, China)

**11:30pm - 12:30pm Keynote Speech "Exploring Server Redundancy in Nonblocking Multicast Data Center Networks ", Yuanyuan Yang (Stony Brook University, USA)**

**12:30am - 01:30pm Lunch Break**

**1:30pm - 3:00pm Architectures and Cloud**

1:30pm "Automated Workload Characterization in Cloud-based Transactional Data Grids", Bruno Ciciani (Sapienza Rome University, Italy), Diego Didona (INESC-ID/IST, Lisbon, Portugal), Pierangelo Di Sanzo, Roberto Palmieri, Sebastiano Peluso, Francesco Quaglia (Sapienza Rome University, Italy), Paolo Romano (INESC-ID/IST, Lisbon, Portugal)

2:00pm "A Usage Control Based Architecture for Cloud Environments", Tina Tavizi, Mehdi Shajari (Amirkabir University of Technology, Theran, Iran), Peyman Dodangeh (Sharif University of Technology, Theran, Iran)

2:30pm "Near-Zero-Downtime Single-Machine Hypervisor Rejuvenation", Dmitry Zakharov (Hasso-Plattner-Institut, University of Potsdam, Germany), Felix Salfner (SAP Innovation Center Potsdam, Germany)

**3:00pm - 3:30pm Coffee Break**

**3:30pm - 5:00pm Dependability Analysis and Verification**

3:30pm "Analysis of Data Reliability Tradeoffs in Hybrid Distributed Storage Systems", Bing Tang, Gilles Fedak (INRIA, ENS Lyon, France)

4:00pm "Component Substitutability Behavior Consistency Verification Based On Architecture-driven Development", Luxi Chen, Linpeng Huang, Chen Li (Shanghai JiaoTong University, China)

4:30pm "Adaptive Mapping of Parallelized Application (fork-join DAG) on Multicore System in the Presence of Multiple Failures", Gilles Bizot (CNRS UJF INP, Grenoble, France), Dimiter Avresky (IRIANC, Boston, USA), Fabien Chaix, Nacer-Eddine Zergainoh, Michael Nicolaidis (CNRS UJF

INP, Grenoble, France)

**5:00pm - 5:15pm Concluding Remarks**

## Theme

Increasingly large and complex parallel, distributed and network-centric computing systems provide unique challenges to the researchers in dependable computing, especially because of the high failure rates intrinsic to these systems. The goal of this workshop in continuation of the FTPDS (Fault-Tolerant Parallel and Distributed Systems) workshop series is to provide a forum for researchers and practitioners to discuss all aspects of dependability including reliability, availability, safety and security for parallel, distributed and network-centric systems. All aspects of design, theory and realization are of interest.

## Topics of interest include, but are not limited to:

- Dependable parallel, distributed and network-centric systems
- High availability in parallel, distributed and network-centric computing systems
- Safety and security in distributed and network-centric computing systems
- Autonomic network computing
- Dependability by virtualization
- Dependable high-speed wide, local, and system area networks
- Dependable mobile computing
- Dependable clusters
- Dependable internet servers
- Dependable cloud computing
- Dependability in distributed embedded systems
- Dependability issues of multi- and manycore systems
- Dependable protocols for distributed and network-centric systems
- Protocol verification and validation
- Practical experiences and prototypes
- Dependability evaluation of parallel, distributed and network-centric systems

## Program Co-Chairs:

Edgar Nett, University of Magdeburg, Germany
Peter Sobe, University of Applied Sciences Dresden, Germany

## Program Committee:

B. Ciciani, University of Roma, Italy
M. Colajanni, University of Modena, Italy
A. Doering, IBM Research Zurich, Switzerland
C. Elks, University of Virginia, USA
I. Gashi, City University London, UK
K. E. Grosspietsch, Fraunhofer IAIS, Germany
S. Ivanov, RT-solutions.de GmbH, Germany
B. Johnson, University of Virginia, USA
R. Khazan, MIT Lincoln Lab, USA
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F. Quaglia, University of Roma, Italy
F. Salfner, SAP-Potsdam, Germany
L. Silva , University of Coimbra, Portugal
W. Steiner, TTTech Computertechnik AG, Viena, Austria
J. Torres, BSC UPS, Barcelona, Spain
C. Trinitis, TU Munich, Germany
T. Tsuchiya - OSAKA University, Japan

The workshop is sponsored by the IEEE Computer Society Technical Committee on Parallel Processing and IRIANC (International Research Institute on Autonomic Network Computing), Boston, USA/Munich, Germany.

## Contact the Program Chair via email:

Edgar Nett, University of Magdeburg, Germany
Peter Sobe, University of Applied Sciences Dresden, Germany

## Steering Committee:

D. Avresky, International Research Institute on Autonomic Network Computing (IRIANC), Boston USA/Munich Germany (Chair)
E. Maehle, University of Luebeck, Germany (Co-Chair)

To submit papers, send the file (at most 8 pages long including figures and references in the IEEE format (see www.ieee.org)) describing original unpublished research by

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# SERE 2012 : The Sixth International Conference on Sofware Security and Reliability

SHARE

Link: http://paris.utdallas.edu/sere12

| | |
|---|---|
| **When** | Jun 20, 2012 - Jun 22, 2012 |
| **Where** | Washington, D.C., USA |
| **Submission Deadline** | Jan 10, 2012 |
| **Notification Due** | Mar 10, 2012 |
| **Final Version Due** | Apr 10, 2012 |

**Categories** security

## Call For Papers

In 2006, the IEEE Reliability Society made a strategic decision to launch an annual conference focusing on security and reliability. This resulted in the establishment of an International Conference on Secure Software Integration and Reliability Improvement (SSIRI). From this year onward, the conference has been renamed to International Conference on Software Security and Reliability (SERE), with SE representing SEcurity and RE for REliability, to make the focus of the conference more easily recognizable.

SERE 2012 is the sixth annual conference which brings together a wide range of researchers and practitioners to present their on-going ideas, experiences, and outcomes of most recent research, and to exchange their best-of-breed practices for developing reliable, secure, and trustworthy software in a more effective and efficient way. It not only allows the academic community to gain an increased awareness of the areas that are vital to the software industry, but also grants practitioners an opportunity to express their needs. The conference will be held at the National Institute of Standards and Technology, an agency of the U.S. Department of Commerce, in Gaithersburg, Maryland, less than one hour from Washington D.C.

TOPICS OF INTEREST
- Security, Reliability, Availability, and Safety of Software Systems
- Fault Tolerance for Software Reliability Improvement
- Modeling, Prediction, Simulation, and Evaluation
- Validation, Verification, and Testing
- Metrics, Measurements, and Analysis
- Secure and Reliable Storage
- Software Penetration and Protection
- Software Vulnerabilities
- Malware Detection and Analysis
- Intrusion Detection and Prevention
- Operating System Security and Reliability
- Mobile and Cloud Computing
- Theory and Practice
- Benchmark and Empirical Studies

IMPORTANT DATES
- November 15, 2011: Workshop proposals due
- January 10, 2012: Regular papers due
- February 15, 2012: Fast Abstract Track due
- February 15, 2012: Student Doctoral Program due
- March 10, 2012: Author notification
- April 10, 2012: Camera-ready & author registration due

SUBMISSION
Submit original manuscripts (not published or considered elsewhere) with a maximum of ten pages (regular and workshop papers), six pages (Student Doctoral Program), and two pages (Fast Abstract Track). Each paper should include a title, and the name and affiliation of each author. Except for the Fast Abstract Track, each submission should also include a 150-word abstract and up to 6 keywords. The format of your submission must follow the guidelines for IEEE conference proceedings. The first author of a Student Doctoral Program submission must be a student. At least one Best Paper Award will be presented. Detailed instructions for paper submission can be found at http://paris.utdallas.edu/sere12.

SPECIAL SECTION OF THE IEEE TRANSACTIONS ON RELIABILITY

Authors of selected best papers from the conference will be invited to submit an extended version to a special section of the IEEE Transactions on Reliability.

GENERAL INQUIRIES

For more detailed and updated information, please refer to http://paris.utdallas.edu/sere12, or contact Professor Shiuhpyng Shieh, Program Chair at ssp@cs.nctu.edu.tw, or Professor W. Eric Wong, co-Chair, Steering Committee at ewong@utdallas.edu.

## Related CFPs

CCS 2011 Communications and Computer Security Journal

ICITIS 2012 The 3rd IEEE International Conference on Information Theory and Information Security

ICCT 2012 2012 14th IEEE International Conference on Communication Technology

CCS 2012 ACM Conference on Computer and Communications Security

SafeComp 2012 SafeComp 2012 - The 31st International Conference on Computer Safety, Reliability and Security

STM 2012 8th International Workshop on Security and Trust Management

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DepCoS-RELCOMEX 2012

iCal Import

Start:
June 25, 2012
End:
June 29, 2012

Address:
The Brunow Palace, Poland

# Seventh International Conference on Dependability of Complex Systems DepCoS-RELCOMEX 2012 Dependability/Reliability, Safety, Maintenance, Intelligent and Soft Computing, Transport, Networks, Computers

The Brunow Palace

**Submissions**:
Dec 02, 2011

**http://depcos.pwr.wroc.pl/**

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CALL FOR PAPERS

Seventh International Conference
on
Dependability of Complex Systems DepCoS-RELCOMEX 2012
Dependability/Reliability, Safety, Maintenance,
Intelligent and Soft Computing, Transport, Networks, Computers

June 25 – 29, 2012, the Brunow Palace, Poland

http://depcos.pwr.wroc.pl

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Dear Colleague,

Let us bring to your kind attention the Seventh DepCoS-RELCOMEX Conference which will be organized by Institute of Computer Engineering, Control and Robotics, Wroclaw University of Technology, at the end of June 2012. Every year since 2006 this event gathers researchers from numerous countries to discuss broad topics of dependability in contemporary complex systems.

We are pleased to announce that in this edition the best selected conference papers will be included in a monograph published by Springer in series “Advances in Intelligent and Soft Computing”, while all other accepted submissions will be included in “Monographs of System Dependability”. Please also note that we continue broadening of the conference scope towards the diverse problems of dependability in contemporary complex systems: general aspects of dependability theory, transport networks, soft computing, theory of information and IT systems, etc. This is the extension that meets requests form our participants.

You are kindly invited to submit a paper and take active part in this event!

CONFERENCE TOPICS

General problems:
- General aspects of systems, networks theory and communication technology
- Dependability theory of networks, systems and its elements
- Artificial Intelligence,
- Theory of information and IT systems

Methodology and tools:
- System modelling for threats, dependability & maintenance
- Evaluation of software, hardware & networks dependability
- Risk analysis, security and quality assurance
- Soft computing methods and software tools for dependability & maintenance analysis
- Efficiency analysis of contemporary systems and networks
- Neural networks, fuzzy logic, genetic algorithms, expert systems for complex systems analysis and modelling

Technologies:
- Security of networks, systems, software & hardware
- Cryptography methods and tools
- Coding and dependability of data transmission
- Fault tolerant computing
- Maintenance & operational planning

- Artificial Intelligence technologies for dependability
- Functional and economic aspects of complex systems

Applications:
- Planning, maintenance and SLA policies in contemporary systems and networks
- Internet dependability and quality of service
- Software dependability, testing, validation and verification
- Transport systems: scheduling, routing and maintenance organization

PUBLICATION
All submissions will undergo a peer-review procedure and the best papers will be published by Springer in “Advances in Intelligent and Soft Computing” volume. All other accepted papers will be included in “Monographs of System Dependability” published by Wroclaw University of Technology since 2010. One paper copy of both monographs is covered by the conference fee.
As the first step prospective authors should register an abstract on our webpage in the MyReview system (this will require creation of an account at http://depcos.iiar.pwr.wroc.pl/index/createaccount; one account can be used to propose many papers).

CONFERENCE VENUE
The conference will take place in Brunow Palace – a picturesque example of XIXth century Classicism architecture of Lower Silesia in Poland and, at the same time, an enjoyable place to visit and spend time well. Please see for yourself at http://www.brunow.pl.

IMPORTANT DATES
Dec. 2, 2011 – abstract registration (http://depcos.pwr.wroc.pl)
Dec. 9, 2011 – acceptance of the abstracts
Jan. 13, 2012 – full paper submitted for revision in electronic form
Feb. 17, 2012 – notification of final acceptance
Mar. 2, 2012 – conference registration
Mar. 9, 2012 – electronic submission of camera-ready papers

COST
The single conference fee will amount to approx. 590 Euro and will cover accommodation, full meals, one paper copy of both conference publications and participation in all events. Those who have participated in previous DepCoS conferences are eligible for discount of 20, 35 or 50 Euro depending on number of earlier events they attended (3, 4 or 5). Additionally, young researches presenting the most valuable contributions will be granted a scholarship in the form of exemption from the fee (this scholarship does not cover travel expenses).

OUR WEBSITE
For complete and up-to date information please visit

http://depcos.pwr.wroc.pl

Looking forward to seeing you at the conference!
With best regards,

DepCoS-RELCOMEX Organising Committee

Some Tags: **Conference**, **Expert Systems**, **maintenance**, **Routing**, **complex systems**, **Cryptography**, **software tools**, **Security**, **Dependability**, **Internet**

# Call for Papers: DCCS and PDS

**What's The Difference Between PDS and DCCS?**

There are two tracks for regular papers, practical experience reports, demonstrations, and panels: DCCS and PDS. Below is a summary of the main differences between these two tracks.

**The Dependable Computing and Communications Symposium (DCCS)** emphasizes research and practice on all aspects of *design and validatio*n of system level dependability and security. This spans system lifecycle phases including architecture, design, verification/validation, and deployment. More details are available at the DCCS Call for Papers below.

**The Performance and Dependability Symposium (PDS)** emphasizes research and practice on all aspects of *assessment and evaluation* of dependable systems. This spans analytical, simulation, and measurement techniques for evaluating performance, dependability, and security assessment in computer and communication systems. More details are available at the PDS Call for Papers below.

Relevant application areas for both tracks include, but are not limited to:

- Fault-tolerant and resilient systems
- Distributed, parallel, clustered and grid systems
- Peer to peer systems and social networks
- Embedded, real-time and cyber-physical systems
- Database and transactional systems
- Operating systems, file and storage systems
- Mobile and multimedia systems
- Sensor, wireless and ad-hoc networks
- Internet and web-based information services and systems
- Secure and intrusion tolerant systems
- Critical infrastructures and mission-critical systems

**Information for authors** is the same for both tracks.

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## Dependable Computing and Communications Symposium (DCCS)

**To submit a paper to DCCS, click HERE**

**Program Committee Chair**
Philip Koopman
Carnegie Mellon University
koopman .at. cmu.edu

All of us depend upon computers in our everyday lives. But, as the complexity of computer-based systems and networks grows, it is ever more challenging to provide resilience to malicious attacks, accidental faults, design errors, and unexpected operating conditions. Beyond that, such systems are increasingly trusted to provide safety, confidentiality, and near-continuous operation. The challenges to providing computing that is truly dependable in all respects are significant, but meeting them is essential to the well-being of corporations, individuals, governments, and our global economy.

The Dependable Computing and Communications Symposium (DCCS) brings together academic and industrial researchers in all aspects of dependability and security for the premier international conference in this field. We seek first-tier papers on all aspects of the research and practice of creating, validating, deploying, and maintaining systems to achieve dependability and security. All aspects of systems are included, such as architecture, networks, hardware, software, and human elements.

We welcome research papers, practical experience papers, and tool descriptions and demonstrations related to dependable computing and communications, including, but not limited to:

- Design and evaluation of novel algorithms, architectures, and techniques for dependable hardware, software, and networking.
- Dependability challenges and solutions in different computer system application areas including, but not limited to: high speed networking, Internet and WWW, databases and transaction processing systems, distributed systems, embedded and real-time systems, SCADA systems, automotive and aerospace systems, wireless and mobile systems, parallel, grid and cloud systems.
- Novel dependability challenges introduced by new computing and communication technologies and application areas such as, virtualization, utility/on-demand/cloud computing, mobile banking and commerce, sensor networks, multi-core systems, virtual worlds, online social networking, and critical infrastructures (e.g., smart power grid).
- Issues, challenges, and solutions in security, trust, survivability, intrusion detection and tolerance, system integrity, critical infrastructure, and safety-critical systems.
- Dependability issues and solutions in autonomic computing, adaptive and self-managing systems, resilient systems, exception handling, and graceful degradation.
- Reliability, testing, validation, and verification for software, hardware, and systems-of-systems.
- Practical experience in deployment and management of dependable systems, and lessons learned from the application of dependability and security theory to practical systems.

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## Performance and Dependability Symposium (PDS)

## Call for Papers

Link to Abstracts Submission

The authors should identify **Technical Discipline** and **Industrial & Service Sector** for their abstract.

### Technical Disciplines

- Consequence Modeling and Management
- Digital I&C and Software Reliability
- Enterprise Risk Management
- Environmental Impact Assessment
- Fire Simulation and Analysis
- Human Reliability Analysis
- Industrial Safety and Accident Analysis
- Lifetime and Ageing Management
- Maintenance Modelling and Optimisation
- Non-probabilistic/Soft Methods in Reliability Analysis
- Occupational Safety
- Operational Experience and Data Analysis
- Phenomena Modeling
- Policy Making and Legislative Issues
- Reliability Analysis and Risk Assessment Methods
- Risk and Hazard Analyses
- Risk Governance and Societal Safety
- Risk Informed Applications
- Risk Informed Licensing and Regulation
- Risk Management in Large Projects
- Risk Perception and Communication
- Safety Assessment Software and Tools
- Safety Culture and Human & Organizational Factors
- Safety Integrity Level (SIL)
- Safety Management and Decision Making
- Structural Reliability Analysis Methods
- Uncertainty and Sensitivity Analysis, Bayesian Methods
- Other (please specify)

### Industrial & Service Sector

- Automotive Engineering
- Aviation and Space
- Biotechnology and Food Industry
- Chemical and Processing Industry
- Civil Engineering
- Crisis and Emergency Management
- Electrical and Electronic Engineering
- Energy Production and Distribution
- Environment and Sustainable Development
- External Hazards and Climate Risks
- Health and Medicine
- Information Security
- Infrastructures
- Insurance and Finance
- IT and Telecommunications
- Manufacturing and Mechanical Systems
- Marine Engineering
- Nanotechnology
- Nuclear Engineering
- Offshore Oil and Gas
- Public Planning and Policy Decisions
- Security and Defense
- Training and Education
- Transportation
- Waste Management
- Other

**Download PSAM 11 & ESREL 2012 Call for Abstracts**



Published on 27.02.2012

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# DMCC 2012 : 4th Workshop on Dependable Many-Core Computing

SHARE

Link: http://hpcs2012.cisedu.info/2-conference/workshops/workshop-02-dmcc

| | |
|---|---|
| **When** | Jul 2, 2012 - Jul 6, 2012 |
| **Where** | Madrid, Spain |
| **Submission Deadline** | Feb 19, 2012 |
| **Notification Due** | Mar 19, 2012 |
| **Final Version Due** | Apr 11, 2012 |

**Categories** dependability fault tolerance

## Call For Papers

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CALL FOR PAPERS: DMCC 2012
4th Workshop on Dependable Many-Core Computing

As part of
The International Conference on High Performance Computing & Simulation
(HPCS 2012)
http://hpcs2012.cisedu.info/

In conjunction with
The 8th International Wireless Communications and Mobile Computing Conference
(IWCMC 2012)

July 2 - July 6, 2012
Madrid, Spain

Paper deadline: February 19, 2012
Notification: March 19, 2012
Final submission: April 11, 2012

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SCOPE AND OBJECTIVES
Today, the integration of many cores is common - and the end of the road is not in sight. These many-core architectures bring up unprecedented capabilities, opportunities, as well as challenges. The more transistors are integrated on a chip and the more cores are introduced, the more faults can be asserted. These faults arise either during fabrication or operation. As the fabrication costs are continuously rising, it is most important to increase the output of a fab by software, architectural, or physical means. This workshop addresses issues and solutions through dependable soft- or hardware design of many-core systems.

The Workshop topics include (but are not limited to) the following:
* Dependable Many-Core Architectures
* Power-Aware Many-Core Design
* Dependable & Secure Many-Core Designs
* Many-Core Development and Design Tools
* Simulation Techniques
* System-Level Many-Core Implementation
* Many-Core Interconnect Technology
* Many-Core System-On-Chip Development
* Reconfigurable Computing and FPGAs
* Design for Testing
* Hardware and Software Debug Facilities
* Many-Core Programming and Optimization
* Application Partitioning and Load Balancing
* Hypervisors and Virtual Machine Technology
* Trusted and Untrusted Environments
* Virtualization for Dependability
* Dependability through Multi-Threading
* Fault Detection on the Physical Layer
* Fault-Tolerant Software Design
* Fault-Tolerant Hardware Design
* Fault-Tolerant HW/SW Co-Design

PAPER SUBMISSIONS

Submitted papers must not have been published or simultaneously submitted elsewhere. Submission should include a cover page with authors' names, affiliation addresses, fax numbers, phone numbers, and authors email addresses. Please, indicate clearly the corresponding author and include up to 6 keywords from the above list of topics and an abstract of no more than 450 words. The full manuscript should be at most 7 pages using the two-column IEEE format. Additional pages will be charged additional fee. Please include page numbers on all preliminary submissions to make it easier for reviewers to provide helpful comments. Paper submission is handled through the Easychair conference system at:

https://www.easychair.org/conferences/?conf=dmcc2012

Only PDF files will be accepted. Each paper will receive a minimum of three reviews. Papers will be selected based on their originality, relevance, technical clarity and presentation. Authors of accepted papers must guarantee that their papers will be registered and presented at the workshop. Accepted papers will be published in the conference proceedings which will be made available at the time of the meeting.

If you have any questions about paper submission or the workshop, please contact the organizers.

IMPORTANT DATES
Paper Submissions: February 19, 2012
Acceptance Notification: March 19, 2012
Camera Ready Papers and Registration Due: April 11, 2012

WORKSHOP ORGANIZERS
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Email: kluge@informatik.uni-augsburg.de

International Program Committee:
All submitted papers will be rigorously reviewed by the workshop technical program committee members following similar criteria used in HPCS 2012.

* Koen de Boesschere, University of Ghent, Belgium
* I. Koren, University of Massachusetts Amherst, USA
* Jens Lisner, TÜV Nord, Germany
* Wei Liu, Intel, USA
* Avi Mendelson, Microsoft R&D, Israel
* Andre Osterloh, BTC Oldenburg, Germany
* Christine Rochange, Institut de Recherche en Informatique de Toulouse, France
* Volkmar Sieh, University Erlangen-Nuremberg, Germany
* Sascha Uhrig, Technical University of Dortmund, Germany
* Osman Unsal, Barcelona Supercomputing Center, Spain

If you have any questions about the HPCS 2012 conference paper submission, please contact Conference Program Chair: Waleed W. Smari, Voice: +1 (937) 681-0098, Email: smari@arys.org or consult the conference web site at http://hpcs2012.cisedu.info/.

## Related CFPs

DESEC4LCCI 2012 Workshop on DEpendable and SEcure Computing for Large-scale Complex Critical Infrastructures

PARMA 2012 3rd Workshop on Parallel Programming and Run-Time Management Techniques for Many-core Architectures

MARC-ONERA 2012 Many-core Applications Research Community Symposium at ONERA'2012

DSN 2012 The 42nd Annual IEEE/IFIP International Conference on Dependable Systems and Networks

EWDCC 2012 1st European Workshop on Dependable Cloud Computing

EDCC 2012 Ninth European Dependable Computing Conference

SEAA 2012
38th Euromicro
Conference

***IMPORTANT DATES***

| | |
|---|---|
| *Workshops/Tutorial Proposals:* | February 24, 2012 |
| *Abstract Submission to Tracks and Sessions:* | March 23, 2012 |
| *Paper Submission to Tracks and Sessions:* | March 30, 2012 |
| *PhD Symposium Submission:* | April 13, 2012 |
| *Notification of Accepted Papers:* | June 15, 2012 |
| *Camera-ready Paper Due:* | July 02, 2012 |
| *Conference:* | September 5-8, 2012 |

***CONFERENCE COMMITTEES***

**SEAA 2012 General Chair:**
Onur Demirors
*Middle East Technical University, Turkey*

**SEAA 2012 Program Chairs:**
Vittorio Cortellessa
*University of L'Aquila, Italy*

Henry Muccini
*University of L'Aquila, Italy*

**SEAA 2012 Organization Chair:**
Altan Kocyigit
*Middle East Technical University, Turkey*

**Publicity Chair:**
Oktay Turetken
*Tilburg University, The Netherlands*

**Webmaster:**
Mahir Kaya
*Middle East Technical University, Turkey*

**SEAA Steering Committee:**
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*Free University of Bozen-Bolzano, Italy*

Stefan Biffl
*Vienna University of Technology, Austria*

Michel Chaudron
*University of Leiden, The Netherlands*

Ivica Crnkovic
*Malardalen University, Sweden*

Paul Grunbacher
*University Linz, Austria*

The Euromicro Conference series on Software Engineering and Advanced Applications (SEAA) is a long-standing international forum for researchers, practitioners from business and industry, and students to present and discuss the latest innovations, trends, experiences, and concerns in the field of Software Engineering and Advanced Applications in information technology for software-intensive systems.

SEAA2012 will take place in Cesme, Izmir, Turkey. Cesme is surrounded by the Aegean Sea in three sides at the very western end of Urla Peninsula and is neighbor of the Sakiz (Chios) Island (Greece). Cesme is accessible by air via Izmir Adnan Menderes International Airport, which is well-connected with direct flights from many European cities and with frequent connecting flights from the main hub Istanbul.

***CONFERENCE TRACKS AND SESSIONS***

- Model-based Development, Components and Services (**MOCS**)
  - Ivica Crnkovic, *Malardalen University, Sweden*
  - Raffaela Mirandola, *Politecnico di Milano, Italy*
- Software Process and Product Improvement (**SPPI**)
  - Stefan Biffl, *TU Vienna, Austria*
  - Rick Rabiser, *Johannes Kepler Univ. Linz, Austria*
- Software Management (**SM**)
  - Abran Alain, *Universite' du Quebec, Canada*
  - Michel Chaudron, *Leiden University, The Netherlands*
- Embedded Software Engineering (**ESE**)
  - Jan Carlson, *Malardalen University, Sweden*
  - Iain Bate, *University of York, UK*
- Cloud Software (**CS**)
  - Keijo Heljanko, *Aalto University, Finland*

***Special Sessions*** include:

- **DANCE**: Distributed Architecture modeling for Novel Component based Embedded systems (inside the MOCS track)
- **EsPreSSE**: Estimation and Prediction in Software & Systems Engineering (inside the SM track)
- **MeSVAM**: Measurement as a Strategy for Software Value Management (inside the SM track)
- **ESEMT**: Emerging Software Engineering & Management Trends: DevOps, Design-Thinking and Lean Startup (inside the SM track)

***SUBMISSIONS & PUBLISHING***

SEAA 2012 encourages the submission of technical research papers and experience reports. Papers must contain original unpublished work, describe significant novel contributions, and provide evidence on the validation of results. In particular, reports on industrial applications are welcome.

IEEE Computer Society Press will publish accepted papers in the conference proceedings and the *IEEE Xplore* Digital library. A selection of the accepted papers will be invited to revise and re-submit extended versions of their conference papers for consideration for publication in a Journal.

For PhD students there will be a full-day doctoral symposium with feedback from senior experts in the SEAA community.

The SEAA conference will provide best papers awards in the categories: Best research paper, best industrial experience paper, & best PhD research plan

All information about the various calls can be found at http://seaa2012.ii.metu.edu.tr
Join the LinkedIn Group for updates: http://www.linkedin.com/groups/Euromicro-SEAA-2012-4205536

## About Magdeburg

Magdeburg has over 1200 years of eventful history. It was once an imperial palatinate, a member of the Hanseatic League and a major Prussian Fortress which has been marked by unimaginable war and destruction. However, Magdeburg allowed the currents of time to take it into a new future full of possibilities; the city was reconstructed and a particular importance was taken to maintenance, care and preservation of cultural goods. Today, Magdeburg host many scientific and research institutes including Fraunhofer, Max-Planck and Leibniz.

## Conference Venue

In the middle of 12km² beautiful city park Herrenkrug lies the historic Herrenkrug Parkhotel. Within the 19th century building resides a first class hotel, which has been selected now 4 times in a row as the top conference hotel in Germany. The "Jugendstil"-style building offers comfortable hotels suites, a large spa area for personal health and conference rooms in an unique ambience. **SafeComp** will take place in the historic "Festsaal" with its industry exhibition just outside in the winter gardens. During conference breaks it is only a couple of steps to picturesque Herrenkrug Park with its 200 years old trees or the river Elbe. In the evening, Magdeburg`s city centre can be reached by a 5 minute ride with the tram line.

## About SafeComp

Since it was established in 1979 by the **European Workshop on Industrial Computer Systems,** Technical Committee 7 on Reliability, Safety and Security **(EWICS TC7), SafeComp** has contributed to the progress of the state-of-the-art in dependable application of computers in safety-related and safety-critical systems. **SafeComp** is an annual event covering state-of-the-art, actual trends and newest research in the areas of safety, security and reliability of critical computer applications. **SafeComp** provides ample opportunity to exchange insights and experience on emerging methods, approaches and practical solutions. It is a single-stream conference without parallel sessions, allowing easy networking.

**Additional information**
www.ovgu.de/safecomp // www.safecomp.org

## Secretariat SafeComp 2012


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# 2012 SafeComp

The 31st International Conference on Computer Safety, Reliability and Security

## VIRTUALLY SAFE – making system safety traceable!

**25. - 28. September 2012**
**Magdeburg, Germany**

**CALL FOR PAPERS**

CENTER FOR DIGITAL ENGINEERING

Computer Systems in Engineering

ERCIM European Research Consortium for Informatics and Mathematics

vdtc Virtual Development and Training Centre

## SafeComp 2012 Key Theme

Today, we are surrounded by socio-technical, embedded systems which have huge influence on our daily life starting from energy distribution, through transportation systems to communication and logistics.
Continuous integration and automation leads to always rising complexity and criticality. Although the importance of system safety is widely recognized, an increasing number of severe accidents shows that new and better methods for designing and assessing safety are needed. One main reason is inadequate understanding of the system and its safety arguments. The leading theme of the 31st **SafeComp** conference will be:

# VIRTUALLY SAFE – making system safety traceable!

In 2012, we specifically address research and experience, which aims at making dependability assessments both more precise and better to understand. Some examples might be model based approaches, simulation and virtual reality, understandable presentation of safety cases and/or methods for ensuring quality of the used safety arguments. A special focus will be given to multi-disciplinary approaches addressing several dependability issues and their interdependencies at the same time.

## Important dates

**Abstract submission:** 05. February 2012
**Full paper submission:** 19. February 2012
**Tutorial proposals:** 30. April 2012
**Notification of acceptance:** 14. May 2012
**Camera-ready submission:** 04. June 2012

## Scope of the Conference

**Dependability related topics include but are not limited to:**

- safety/security risk assessment
- model-based design and analysis
- risk reduction and mitigation techniques
- self-healing and resilience
- formal methods, verification and validation
- in-the-loop and model-based testing
- methods for qualification and certification
- dependability analysis using simulation and virtual reality
- human factors in dependability analysis
- cyber-physical threats and vulnerability analysis
- safety guidelines, standards and certification
- safety and security interactions
- traceability & saftey cases

**Domains of application include but are not limited to:**

- networking and telecommunication
- railway systems, automotive, aerospace and avionics
- logistics, production automation and robotics
- medical devices and e-health
- critical infrastructures and power systems
- off-shore technology
- education & training

### Paper submission details

The tradition of **SafeComp** is to act as platform for bringing together academic research and industrial needs.
Therefore, industrial contributions and real-world experience reports are explictly invited. We solicit two types of papers: **academic papers** (up to 12 pages) and **industrial experiences papers** (up to 8 pages). All papers will be reviewed by at least three program committee members. Papers must not have been previously published or concurrently submitted elsewhere. All accepted papers will be published by Springer in the **LNCS series.** In addition, it is planned to invite the best contributions to a special issue of the **International Journal on Reliability Engineering and System Safety** (RESS) published by Elsevier.

## International Program Committee

Anderson S. (UK)
Anderson T. (UK)
Bitsch F. (DE)
Bloomfield R. (UK)
Bologna S. (IT)
Bondavali A. (IT)
Braband J. (DE)
Broy M. (DE)
Buth B. (DE)
Damm W. (DE)
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**Francesca Saglietti (Univ. of Erlangen-Nuremberg, Germany)**

**General Chair:**
**Frank Ortmeier (Otto-v.-Guericke-Univ. Magdeburg, Germany)**

**Program Co-Chairs:**
**Frank Ortmeier (Otto-v.-Guericke-Univ. Magdeburg, Germany)**
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**Industry Chair:**
**Michael Schenk (Fraunhofer IFF, Germany)**
**Local Chairs:**
**Gunter Saake (Otto-v.-Guericke-Univ. Magdeburg, Germany)**
**Jana Dittmann (Otto-v.-Guericke-Univ. Magdeburg, Germany)**

SIEMENS AIT AUSTRIAN INSTITUTE OF TECHNOLOGY OESTERREICHISCHE COMPUTER GESELLSCHAFT AUSTRIAN COMPUTER SOCIETY TÜV NORD European Network of Clubs for REliability and Safety of Software GfSE Gesellschaft für Systems Engineering e.V. German Chapter of INCOSE

## Call For Papers

# IEEE SRDS 2012

## 31st International Symposium on Reliable Distributed Systems

## October 8-11, 2012, Irvine, California, USA

**http://web.mst.edu/~cswebdb/srds2012/**

**Symposium Co-Chairs**
Sanjay Madria, Missouri S&T, USA
Sharad Mehrotra, University of California-Irvine, USA

**Technical Program Committee Co-Chairs**
Takahiro Hara, Osaka University, Japan
Leszek Lilien, Western Michigan University, USA
Istvan Majzik , Budapest University of Technology and Economics, Hungary

**Finance Chair**
Sriram Chellappan, Missouri S & T, USA

**Local Arrangements Co-Chairs**
Phillip Sheu, University of California-Irvine, USA
George Wang, California State University, USA

**Publication Chair**
Biswajit Panja, University of Michigan-Flint, USA

**Best Paper Award Co-Chairs**
Anoop Singhal, NIST, USA
Atul Prakash, University of Michigan, USA

**Workshop Co-Chairs**
Pin Zhou, IBM Almaden, USA
Ravi Prakash, University of Texas-Dallas, USA

**PhD Forum Co-chairs**
Aniruddha Gokhale, Vanderbilt University, USA
Qi Han, Colorado School of Mines, USA

**Publicity Co-Chairs**
Nayot Poolsappasit, Missouri S & T, USA
Anirban Mondal, IIITD, India

**SC Liaisons**
Bharat Bhargava, Purdue University, USA

The Symposium on Reliable Distributed Systems is a forum for researchers and practitioners interested in distributed systems design, development and evaluation, with emphasis on reliability, availability, safety, security, trust and real time. We welcome original research papers as well as practical experience reports that deal with design, development and experimental results of operational systems.

### Topics of Interest

The major areas of interest include, but are not limited to, the following topics:

* Cloud computing and virtualization
* Autonomic, pervasive, and ubiquitous computing
* Secure and trusted storage systems
* Secure and dependable web services
* High-confidence and safety-critical systems
* Parallel and distributed operating systems
* Distributed objects and middleware systems
* Fault-tolerant and secure sensor networks
* Event-based processing and peer-to-peer infrastructures
* Distributed databases and transaction processing
* Distributed measurement, monitoring, and predictions
* Wireless ad hoc networks
* Electronic commerce and enabling technologies
* Formal methods and foundations for dependable distributed computing
* Analytical or experimental evaluations of dependable distributed systems
* Internet-based systems and applications
* Scalable systems design
* QoS control and assessment
* Trust and scalable system design in social networks
* Social media and privacy issues

### Submission Guidelines

Papers must be written in English. Regular Papers describing original research as well as design, development and experimental results of operational systems, and Practical Experience Reports describing on-going industrial projects, prototype systems and exploratory or emerging applications, should be no longer than 10 pages, following the IEEE two-column format for conference proceedings, in font size no smaller than 10 points, and must fit properly on US "Letter-size" paper (8.5 x 11 inches). All papers that deviate from these specifications will be rejected automatically. Authors are requested to first register their submissions and submit their manuscripts in PDF format. Submission details can be found at http://web.mst.edu/~cswebdb/srds2012/. Accepted papers will appear in the symposium proceedings, which will be published by the IEEE Computer Society.

### Best Paper Award

All papers accepted for presentation at the symposium will be evaluated by the Best Paper Award Co-Chairs for the best paper award.

### Workshops

Workshops will be held in conjunction with SRDS 2012 on 8th/11th October, 2012.

### Submission Deadlines

Abstract submission: 26th March 2012 Paper submission : 2nd April 2012
Author Notification: 20th June 2012 Camera-ready: 16th July 2012

# ISSRE 2012

**Nov 27-30, 2012**
**Dallas, TX USA**


**Conference**

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## Call For Papers ISSRE 2012

The 23rd annual International Symposium on Software Reliability Engineering (ISSRE 2012) is focused on innovative techniques and tools for assessing, predicting, and improving the reliability, safety, and security of software products. ISSRE also emphasizes industrial relevance, rigorous empirical studies and experience reports. Topics of interest include, but are not limited to:

- Reliability, availability, and safety of software systems
- Validation and verification, testing
- Software quality
- Software security
- Fault tolerance, survivability, and resilience of software systems
- Systems (hardware + software) reliability engineering
- Services reliability engineering
- Open source software reliability engineering
- Metrics and measurements, estimation, prediction of quality/reliability
- Supporting tools and automation
- Industry best practices
- Empirical studies of any of the above topics

# 2013 6th Latin-American Symposium on Dependable Computing (LADC)

Printer Friendly | Confirm IEEE Record

Recommendation for you

1. 2013 6th Latin-American Symposium on Dependable Computing (LADC) (LADC)
2. 10th Latin American Theoretical Informatics Symposium (LATIN)
3. Latin American Symposium on High Performance Computing (HPCLatAm)
4. Latin American Web Congress (LA-WEB)
5. 1st Latin-American Conference on Bioimpedance (CLABIO)

1**IEEE Conference Record #:** 20386

**Conference Title:** 2013 6th Latin-American Symposium on Dependable Computing (LADC)

**Conference Acronym:** LADC

**Conference Dates:** 4/1/2013 to 4/5/2013

**Location:** TBD

**City:** Rio de Janeiro

**Country:** Brazil

**State/Province:**

**Exhibits:** N **No. of Exhibits:** 0 **Tutorials:** N **Attendance:** 90 **Producing Publication:** N **Concentration Banking Info:** Y

**Conference Scope:** The symposium scope spans system, software and hardware dependability research and technological challenges, addressing the topics of dependability in: systems engineering; fault tolerant architectures, multi-core systems, virtualization, nanoscale computers, architectures, runtime monitoring, model driven engineering, testing, verification, certification, networks protocols, databases, human-computer interaction, security, safety, methods, assessment, measurement and benchmarking.

**Conference Keywords:** latin, american, dependable, computing, ladc, dependability, security, safety, dependability of network, dependability of data, critical systems, fault tolerant system, dependability benchmark

**Conference Focus:** Scientific/Academic

**WWW URL:** www.ft.unicamp.br/ladc2013

**CFP URL:** www.ft.unicamp.br/ladc2013

**Abstract Submission Date:**

**Notification of Acceptance:**

**Final Paper Submission Date:**

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**Keywords**: Accepted papers list. Acceptance Rate. EI Compendex; Engineering, ISTP, ISI index, Impact Factor.
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