

Efficient implementation of unified ECC accelerators based on the Karatsuba multiplication method

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Modern communication systems rely heavily on cryptography to ensure authenticity, confidentiality and integrity of exchanged messages. Elliptic Curve Cryptography (ECC) is an asymmetric cryptographic approach and is used in a various fields of application. ECC is one of the commonly used standard methods for sharing secret keys, for signing messages and for authentication. In this paper we present our ASIC implementation of a design supporting four different NIST Elliptic Curves 186-4 (2015). The design supports two B-curves over $GF(2^n)$ (B-233, B-283) and two P-curves over $GF(p)$ (P-224, P-256). Sharing the hardware components – bus, multiplier and registers – reduces the area of the kP accelerator significantly. The core component that enables this type of a combined design is our unified multiplier used for both types of finite fields. As far as we know it is the first time that the 4-segment Karatsuba multiplication method [Date-2005] is used for multiplication of elements of $GF(2^n)$ as well as of elements of $GF(p)$. Compared to the classical multiplication method that needs 16 partial product calculations the 4-segment Karatsuba multiplication method needs to calculate only 9 partial products. The calculation of each partial product requires a single clock cycle in our implementation. We implemented the writing of inputs to the field multiplier and reading the field product in parallel to the calculation of the partial products. Thus, the applying the 4-segment Karatsuba multiplication method decreases the calculation time of a field product by about $\frac{(16-9)}{16} \cdot 100\% = 44\%$.

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References

FEDERAL INFORMATION PROCESSING STANDARD (FIPS) 186-4 (2015). Digital Signature Standard; Request for Comments on the NIST-Recommended Elliptic Curves.