

Compositional Analyses of Highly-Configurable Systems with Feature-Model Interfaces

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Abstract: Today's software systems are often customizable by means of load-time or compile-time configuration options. These options are typically not independent and their dependencies can be specified by means of feature models. As many industrial systems contain thousands of options, the maintenance and utilization of feature models is a challenge for all stakeholders. In the last two decades, numerous approaches have been presented to support stakeholders in analyzing feature models. Such analyses are commonly reduced to satisfiability problems, which suffer from the growing number of options. While first attempts have been made to decompose feature models into smaller parts, they still require to compose all parts for analyses. We proposed the concept of a feature-model interface that only consists of a subset of features and hides all other features and dependencies. Based on a formalization of feature-model interfaces, we proved compositionality properties. We evaluated feature-model interfaces using a three-month history of an industrial feature model with 18,616 features. Our results indicate performance benefits especially under evolution as often only parts of the feature model need to be analyzed again.

Keywords: Configurable Software, Software Product Line, Product-Line Analysis, Product-Line Evolution, Variability Modeling, Feature Model, Modularity, Compositionality

Overview

In this talk, we give an overview on our past and on-going research efforts towards compositional analyses of software product lines. We argue that our notion of feature-model interfaces serves as a foundation for many future analyses of product lines. Feature models are used in most product-line analyses, and especially in family-based analyses [Th14]. Hence, compositional feature modeling is the key towards better scalability.

The main results on feature-model interfaces have been presented at the International Conference on Software Engineering in Austin, Texas [Sc16]. We proved that a set of feature-model analyses can be modularized using feature-model interfaces in a sound and complete manner. Furthermore, we evaluated the benefit on the three-month history of an automotive feature model with 18,616 features, which we decomposed into 40 submodels. We measured large speed-ups with feature-model interfaces, which are due to the fact that feature-model analyses are reduced to boolean satisfiability problems and feature-model interfaces effectively reduce the number of boolean variables.

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Besides pure analysis of feature models, we also proposed how to incorporate feature-model interfaces into the verification of domain artifacts. At the International Workshop on Variability Modeling of Software-Intensive Systems in Salvador, Brazil, we have presented benefits for theorem proving with KeY [Th16].

While we measured speed-ups for analyses using feature-model interfaces, the computation of feature-model interfaces turned out to be a bottleneck. Existing approaches to eliminate features [Th11, Ac11] did not scale to our large industrial models. We improved those algorithms and evaluated which parameters overcome this bottleneck, as presented at the International System and Software Product Line Conference in Beijing, China [Kr16].

At the International Workshop on Feature-Oriented Software Development in Amsterdam, The Netherlands, we presented another application of feature-model interfaces. In particular, we derived and explained implicit constraints for any given subtree of the feature model, which are otherwise unknown in large feature models [An16].

In our on-going work, we evaluate whether other analyses, such as type checking, can also be improved with feature-model interfaces. It seems that we identified a further bottleneck, which is the composition of several feature-model interfaces. The composition is needed if a query contains features of several feature-model interfaces and the conjunction of satisfiability solvers or binary decision diagrams are quite expensive operations.

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